

15

vi) a correlator controller for receiving an output of said long code timing detector and for controlling the action of said plurality of correlators; and

vii) a spread code controller for receiving an output from said long code synchronization judging means, and for outputting control signals to select a spread code sequence used for correlating action in said matched filter and said plurality of correlators.

3. A receiver for the asynchronous DS-CDMA cellular communication system as claimed in claim 2, wherein said plurality of correlators further comprises a delay-locked loop for performing acquisition of a received spread spectrum signal, said correlator controller controls the action of said plurality of correlators according to the peak location of the correlated output from said matched filter, and the data output from said plurality of correlators and from said matched filter are judged through RAKE processing.

4. A receiver for the asynchronous DS-CDMA cellular communication system as claimed in claim 2, wherein after long code synchronization, a traffic channel signal is received using said matched filter and said plurality of correlators, and multipath signal data are judged after RAKE processing.

5. A receiver for the asynchronous DS-CDMA cellular communication system as claimed in claim 3, wherein after long code synchronization, a traffic channel signal from the base station of the present cell is received using said matched filter and some correlators among said plurality of correlators, RAKE processing is performed, a control channel signal from the base station of a peripheral cell is

16

received using the remaining correlators, the long code of said cell is recognized and synchronized, a data signal the same as that from the base station of the present cell is received, and signals from both base stations or a plurality of base stations are judged.

6. A receiver for the asynchronous DS-CDMA cellular communication system as claimed in claim 3, wherein a multipath signal contained in said matched filter undergoes RAKE processing to communicate with the base station of the present cell using said matched filter, and a long code is divided into the length of a short code to detect the long code corresponding to said present cell or said peripheral cell by said matched filter, and correlations are sequentially detected by every symbol.

7. A receiver for the asynchronous DS-CDMA cellular communication system as claimed in claim 2, wherein said matched filter comprises a plurality of multiplication circuits for outputting the output of a plurality of sampling and holding circuits to the first and second output terminals according to the bit value corresponding to the spread code sequence, a first analog addition circuit for adding the output of the first output terminal of each said multiplication circuit, a second analog addition circuit for adding the output of the second output terminal of each said multiplication circuit, and a third analog addition circuit for calculating the difference between the outputs of said first and second analog addition circuits.

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